2nd Annual Workshop on Fault-Tolerant Parallel and Distributed Systems

April 5, 1997 Geneva, Switzerland

Message from the General Co-Chairs

It is our pleasure to welcome you to the 2nd Fault-Tolerant Parallel and Distributed Systems (FTPDS97) workshop in Geneva.

Increasingly large parallel computing systems provide unique challenges to the researchers in dependable computing, especially because of the high failure rates intrinsic to these systems.

While commercial and scientific companies share the need for massive throughput and low latency, dependability of service is also a concern.

The response to our call for participation was good due to the efforts of the program committee members. We are deeply indebted to them for the commitment to the review of the papers.

We would like to thank all panelists from academia and leading research institutions for their participation and willingness to share their latest results with our community.

We would also like to extend a special thanks to the members of the Network Computing Laboratory at Boston University for their help in organizing this workshop.

General Co-Chairs:

Dimiter R. Avresky
Department of Electrical and Computer Engineering
Boston University
44 Cummington St.
Boston, MA, 02215
email: avresky@bu.edu

David R. Kaeli
Department of Electrical and Computer Engineering
Northeastern University
409 Dana Research Center
Boston, MA, 02115
email: kaeli@ece.neu.edu

Workshop on Fault-Tolerant Parallel and Distributed Systems

Distributed Safety-Critical Systems Using Commercial Off The Shelf Components Barry W. Johnson (University of Virginia, U.S.A.)

Dependability and other challenges in the collision between Computing and Telecommunication

Y. Levendel (Lucent Technologies Bell Labs Innovations, U.S.A.)

Resource Harvesting in Scalable, Fault-tolerant. Single System Image Clusters

Randy Alexander, Rory Foster, Brad Benton, Luke Young (Tandem Computers Incorporated, U.S.A.)

A Unified Approach for the Synthesis of Scalable and Testable Embedded Architectures

P.B. Bhat, C. Aktouf, V. K. Prasanna, S. Gupta and M. A. Breuer (University of Southern California, U.S.A.)

A Fault-Robust SPMD architecture for 3DTV Image Processing

A. Chiari, B. Ciciani, M. Romero (University of Rome "La Sapienza", Italy)

An Efficient and Recoverable DSM on a Network of Workstations: Design and Implementation

A.-M. Kermarrec, C. Morin Vrije (Universiteit, The Netherlands)



Fault-Tolerant Dynamic Task Scheduling Based on Dataflow Graphs

E. Maehle, F.-J. Markus (Mediziniche (Universitat zu Lubeck, Institut fur Technische Informatik, Germany)

http://www.iti.mu-luebeck.de/p/markus.html

Using Static Total Causal Ordering Protocols to Achieve Ordered View Synchrony

M. Iyer and K.-Y. Siu D'Arbeloff (Laboratory for Information Systems and Technology, MIT, U.S.A.)

Revisiting the Non-Blocking Atomic Commitment Problem in Distributed Systems

M. Raynal (IRISA, France)

A Meta-object Architecture for Fault Tolerant System

J.-C. Fabre (LAAS-CNRS & INRIA, France)

A Fail-Aware Datagram Service

C.Fetzer, F. Cristian (University of California, U.S.A.)

A checkpointing-recovery scheme for domino free distributed systems

F.Quaglia, B. Ciciani and R. Baldoni (Universita "La Sapienza", Italy)

Dynamic Fault Recovery for Wormhole-Two-Dimensional Meshes

C.M. Cunningham (Texas A & M University, U.S.A.) and D.R. Avresky (Boston University, U.S.A.)

Reliability Evaluation of A Task Under A Hardware Fault-Tolerant Technique

O.A. Abulnaja, S.H. Hosseini and K.Vairavan (University of Wisconsin-Milwaukee, U.S.A.)

Fault Tolerance Measures for m-ary n-dimensional Hypercubes Based on Forbidden Faulty Sets

J. Wu and G. Guo (Florida Atlantic University, U.S.A.) http://www.cse.fau.edu/~jie